## The Design Process, Part 7

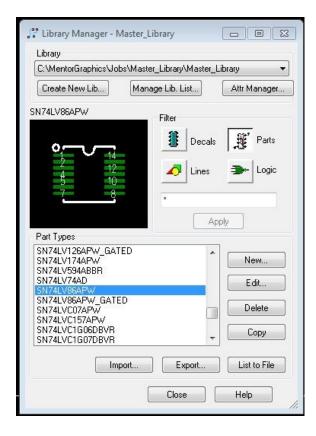
## Building the Library: Drawing things in the library - Multi-Gated Parts

Sharon Hurtley-Durand 7 January 2022

Single Gated Parts can be used in situations where all of the connecting wires to associated parts are in the immediate vicinity on the schematic. But, this is not always the case. Often the parts associated with the Gated Part are in other areas and on other pages, so it is best in these circumstances to break the part down into its individual Gates.

When I build a part that will often be used broken out into its gates, I always build it first as a single gated part so that it's available when needed.

Here we will compare an IC as a single gated part and as a multi-gated part. This SN74LV86APW contains four 2-Input Exclusive-Or Gates and two Signal Pins. To an engineer, these strange sounding things will make sense. A layout person doesn't need to know what magic goes on inside these gates, but any additional knowledge about their functions doesn't do any harm. Whether the part is kept as one gate or broken out into its individual parts makes no difference when it's on the PC Board as the same footprint is used for both. Here they are side by side. And at this stage they look identical except for their names.



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When we look at the General information about the part we see that the single gated part has a Gate Count of 1 and the multi-gated part shows a count of 5. Now, note that the description of the part said that it had four 2-Input Exclusive-Or Gates and two Signal Pins. Here, I have created a gate that incorporates the two signal pins, thus not leaving them off the schematic.

Part Statistics	cals Gates Pins Attributes Co	Logic Family
1 dit Statistics		Logic Family
Pin Count:	14	IC 👻
Decal:	PW-14	
Gate Count:	1	Ref Prefix: U
Signal Pin Cou	nt: O	Families
Options	ing of Part Type pin numbers to PCB D	ecal Special Purpose
ECO Registe		Connector
		💮 Die Part
Prefix List:		Flip Chip

So here, the Signal Pins Count is 0. And their connections to Power and Ground will be visible in the last gate.

Part Statistics	14	Logic Family
	PW-14	
Gate Count:	5	Ref Prefix: U
Signal Pin Count:	0	Families
Options		
Define mapping	of Part Type pin numbers to PCB Decal	Special Purpose
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Prefix List:		O Die Part
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Here in PCB Decals, we see that both the single gated part and the multi-gated part are both assigned a PW-14 Decal. So, to emphasize, there is no difference on the PC Board Layout half of the software whether the single gated or the multi-gated part is used.

	Gates	rins   Attribute	s Connector Pin M	ahhirið I	
PW-14		Library	:		Reset
0,-		<b></b>		aster_Library\Master_Lit	orary 🔻
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9	8			14	Apply
	,		Show only Dec	als with pin numbers ma	tching Part Type
Unassigned De	cals:	10	Assig	ned Decals:	
3M-30314-600	2HB		PW-	14	Assign New
3M-X2514-X0>		ш			/ saigh New
	BOARD NO S		Assign >>		201
	bo/(10_10_0				Up
BRONCO-ON- D-14			<< Unassign		
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BRONCO-ON- D-14 DGV-14 DIP-7X2P-100					DOWN
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	Attributes Connector Pin M	Denidation 1	
PW-14	- 25 Yest 10		Reset
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6 8	2 <b>4</b> 3	14	Apply
· · · · · · · · · · · · · · · · · · ·	Show only Dec	cals with pin numbers matc	hing Part Type
Unassigned Decals:	Assig	gned Decals:	
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BRONCO-ON-BOARD BRONCO-ON-BOARD_NO_SILK	Assign >>		
D-14			Up
DGV-14 DIP-7X2P-100	<< Unassign		
DIP14			Down
ED1512			ST
	18 (D)	19	

In the Gates window, we see the difference between these parts. And how very different they look. The four 2-Input Exclusive-Or Gates, called XOR, each have 3 Pins from the Part and the two Signal Pins are built into the last Gate.

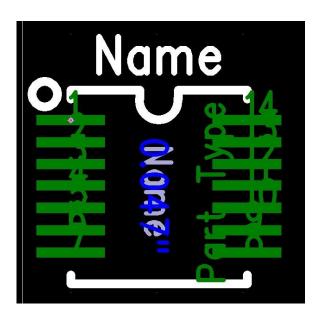
	PCB De	ecals Ga	ates Pins Attr	ributes Connector	Pin Mapping		
						Reset	
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Gate	Pins	Swap	CAE Decal 1	CAE Decal 2	CAE Decal 3	CAE Decal 4	
	[ 3255	100	DIP-14				
A	14	0	211 14				
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A	14	U	211 14				
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A	14	U	Un 17				

	PCB De	ecals Ga	ates   Pins   Attr	ibutes Connector	Pin Mapping	Reset	
Gate	Pins	Swap	Edit CAE Decal 1	Add CAE Decal 2	Delete CAE Decal 3	CAE Decal 4	
A	3	0	XOR				1
В	3	0	XOR				
С	3	0	XOR				
D	3	0	XOR				
E	2	0	DEVICE-2				

Here is a view of the last Gate, called E, DEVICE-2, which has the last 2 Pins of the part.

				ibutes Connector		Reset	
						Reset	
			Edit	Add	Delete		
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Gate	Pins	Swap	CAE Decal 1	CAE Decal 2	CAE Decal 3	CAE Decal 4	
A	3	0	XOR				
В	3	0	XOR				
С	3	0	XOR				
D	3	0	XOR				
E	2	0	DEVICE-2				

The Pin windows on the following page, show the dramatic difference between these Parts and how their gates are assigned.



In the single gated part the Pin Numbers correspond to the footprint starting at the top left as viewed in the Gates window of the single gate part and continue counter-clock wise around the part.

#1:TYP=U SWP=0 NETNAME #E	#E NETNAME #14:TYP=U SV	VP=
#2:TYP=U SWP=0 NETNAME #E	#E NETNAME #13:TYP=U SV	VP=
#3:TYP=U SWP=0 NETNAME #E	#E_NETNAME #12:TYP=U SV	VP=
#4:TYP=U SWP=0 NETNAME #E	#E NETNAME #11:TYP=U SW	VP=
#5:TYP=U SWP=0 NETNAME #E	#E NETNAME #10:TYP=U SV	VP=
#6:TYP=U SWP=0 NETNAME #E	#E_NETNAME #9:TYP=U SWI	P=0
#7:TYP=U SWP=0 NETNAME #E	#E_NETNAME #8:TYP=U SW	P=0

To the left is the PW-14 Decal and to the right is the DIP-14 Logic Symbol.

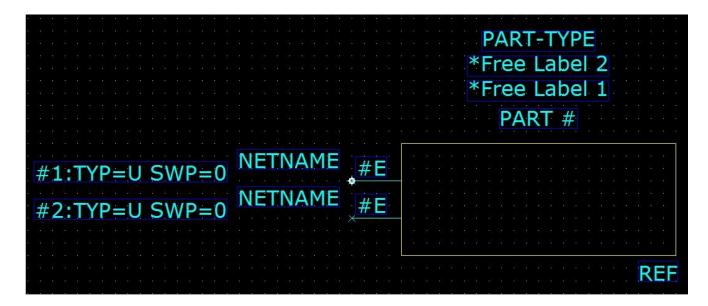
These Pin Names are derived from the Data Sheet on this part and will show up internally on the drawing of the Logic symbol. Within the Name, the preceding numbers 1 through 4 identify the four physical function gates that are within the part. The letters A and B represent the inputs to those gates and the letter Y represents the output. VCC is the nomenclature for Power and GND indicates Ground. Here, since there is only one Logic Symbol, the Pin Numbers and Pin Sequences are the same.

Pin Group	Number	Name	Туре	Swap	Seq.	Reset
Gate-A	1	1A	Undefined	1	1	
Gate-A	2	1B	Undefined	1	2	Edit
Gate-A	3	1Y	Undefined	1	3	
Gate-A	4	2A	Undefined	1	4	Add Pin
Gate-A	5	2B	Undefined	1	5	
Gate-A	6	2Y	Undefined	1	6	Add Pins
Gate-A	7	GND	Undefined	1	7	
Gate-A	8	3Y	Undefined	1	8	Delete Pins
Gate-A	9	3A	Undefined	1	9	Renumber
Gate-A	10	3B	Undefined	1	10	Theriamber
Gate-A	11	4Y	Undefined	1	11	
Gate-A	12	4A	Undefined	1	12	Сору
Gate-A	13	4B	Undefined	1	13	
Gate-A	14	VCC	Undefined	1	14	Paste
						Import CSV

Here is the Logic Symbol for the Exclusive-Or Gate. The numbering on these pins do not correlate to the pin numbers of the part. In these situations, they are the sequential pin numbers within the gate. The same is true for the DEVICE-2 Gate.

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#1:TYP=U SWP=0		<u>Да</u> риян калада к <u>к</u>		
		-]- { <b>-</b> - <b>#</b>	E #3:TYP=	-11 C\M/D=0
e ende le le recenter de le recenter de le rece	an a		L.#3.11F-	-0 SWF=0
#2:TYP=U SWP=0	NETNAME #E	r}-}-,,/,/		
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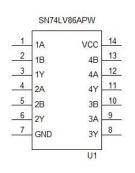
And below is the Logic Symbol for the DEVICE-2 Gate.

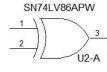


Here is the Pin Information for the Gated Part. Gate-A is the first physical function gate within the part. Its inputs A and B are on Pins 1 and 2 of the part and correspond to the Sequences 1 and 2 on the Logic Symbol shown above. The output Y is on pin 3 of the part and it is on Sequence 3 of the Logic Symbol. Gate B shows the same sequential order and correlates to Pins 4, 5 and 6 of the part. Gates C and D have a slightly different sequential order because of how the functions are distributed within the physical part. And, lastly Gate-E picks up the two Signal Pins of Power and Ground. Arbitrarily, I have assigned VCC to Pin 1 as the power symbol is normally shown pointing up for positive power and GND is normally shown pointing down.

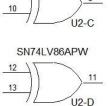
Pin Group	Number	Name	Туре	Swap	Seq.	Reset
Gate-A	1	1A	Undefined	1	1	
Gate-A	2	1B	Undefined	1	2	Edit
Gate-A	3	1Y	Undefined	1	3	
Gate-B	4	2A	Undefined	1	1	Add Pin
Gate-B	5	2B	Undefined	1	2	
Gate-B	6	2Y	Undefined	1	3	Add Pins
Gate-C	9	3A	Undefined	1	1	
Gate-C	10	3B	Undefined	1	2	Delete Pins
Gate-C	8	3Y	Undefined	1	3	Renumber
Gate-D	12	4A	Undefined	1	1	Trendinber
Gate-D	13	4B	Undefined	1	2	
Gate-D	11	4Y	Undefined	1	3	Сору
Gate-E	14	VCC	Undefined	1	1	
Gate-E	7	GND	Undefined	1	2	Paste
						Import CSV

Here is an example of the single gated part and the multiple gated part placed on the schematic. I have placed typical power and ground attachments to Gate-E. Here, because of better viewing, the visibility of the Pin Names has been turned off in Gates A through D (top picture). It is often difficult to have control of where the software places the pin names in the gates and their placement can conflict with the drawn lines as shown in the bottom picture. Here also, I have added Net Names to the Wires.



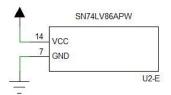




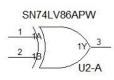


SN74LV86APW

9



1 1A	VCC	14
2 1B	4B	13
3 1Y	4A	12
4 2A	4Y	11
5 2B	3B	10
6 2Y	3A -	9
7 GND	3Y	8



SN74LV86APW

4 2 5 U2-B

9 37 8 10 38 U2-C

SN74LV86APW

SN74LV86APW

